

FIG. 1A

3RD DIFFUSION LAYER 7  
1ST DIFFUSION LAYER 4  
2ND DIFFUSION LAYER 5

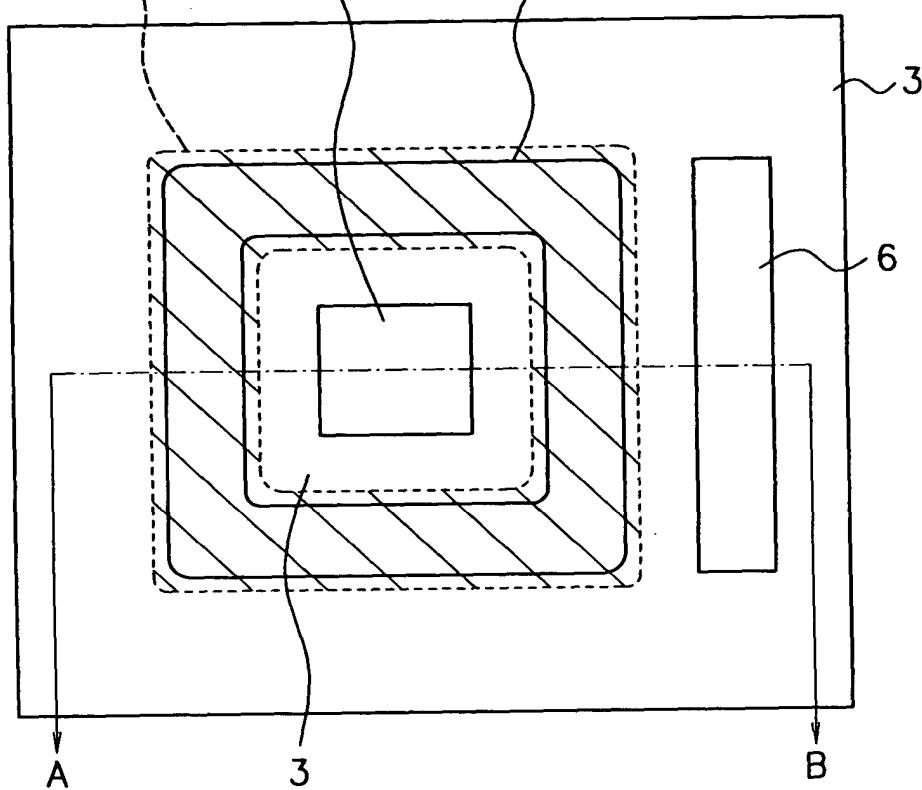


FIG. 1B

I/O TERMINAL 8  
DEVICE ISOLATING FILM 3

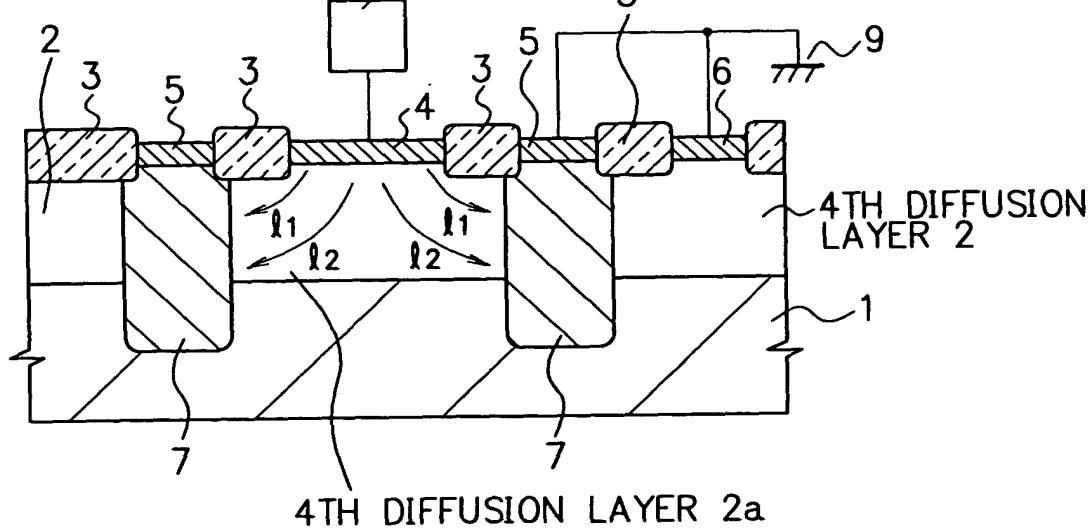


FIG. 2

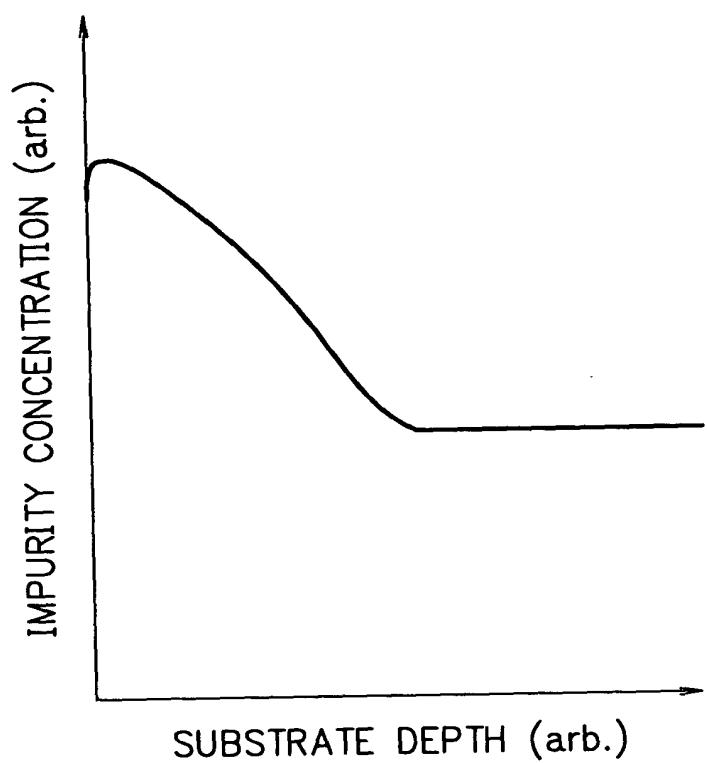


FIG. 3A

3RD DIFFUSION LAYER 7a GATE ELECTRODE 10

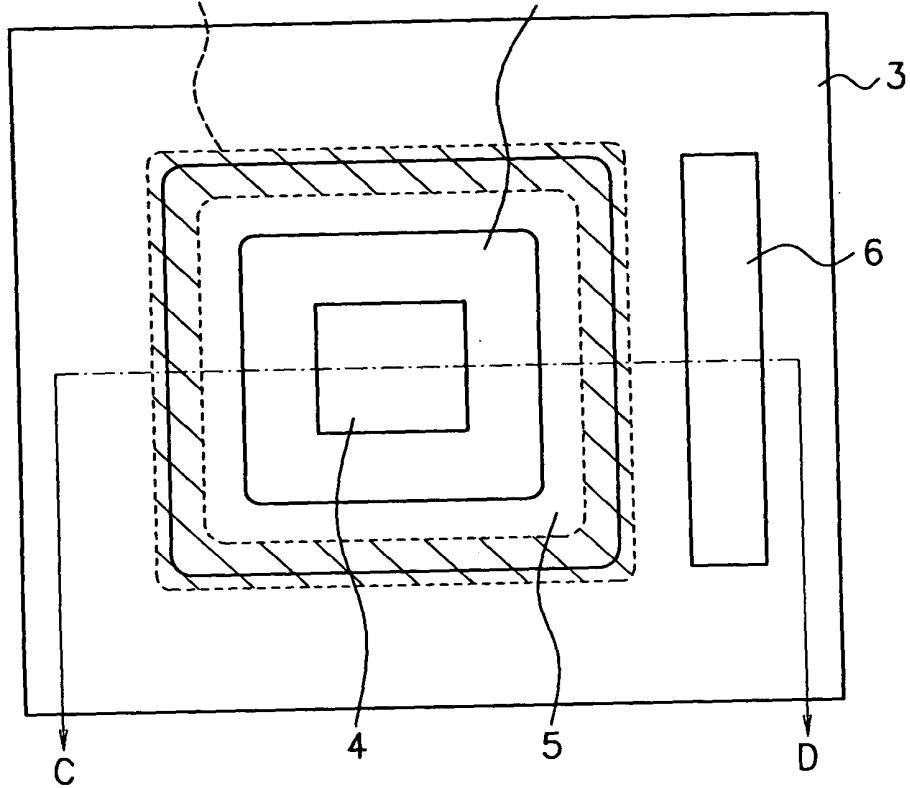
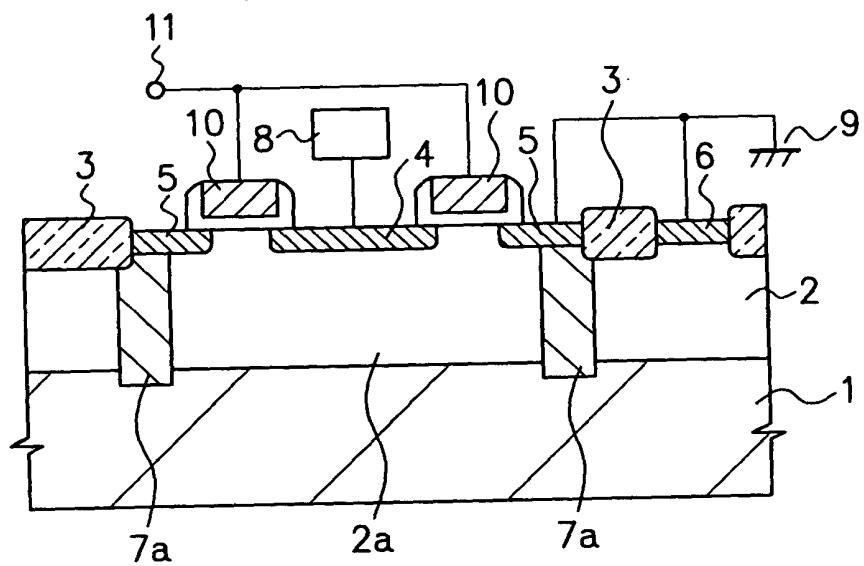


FIG. 3B



F I G. 4

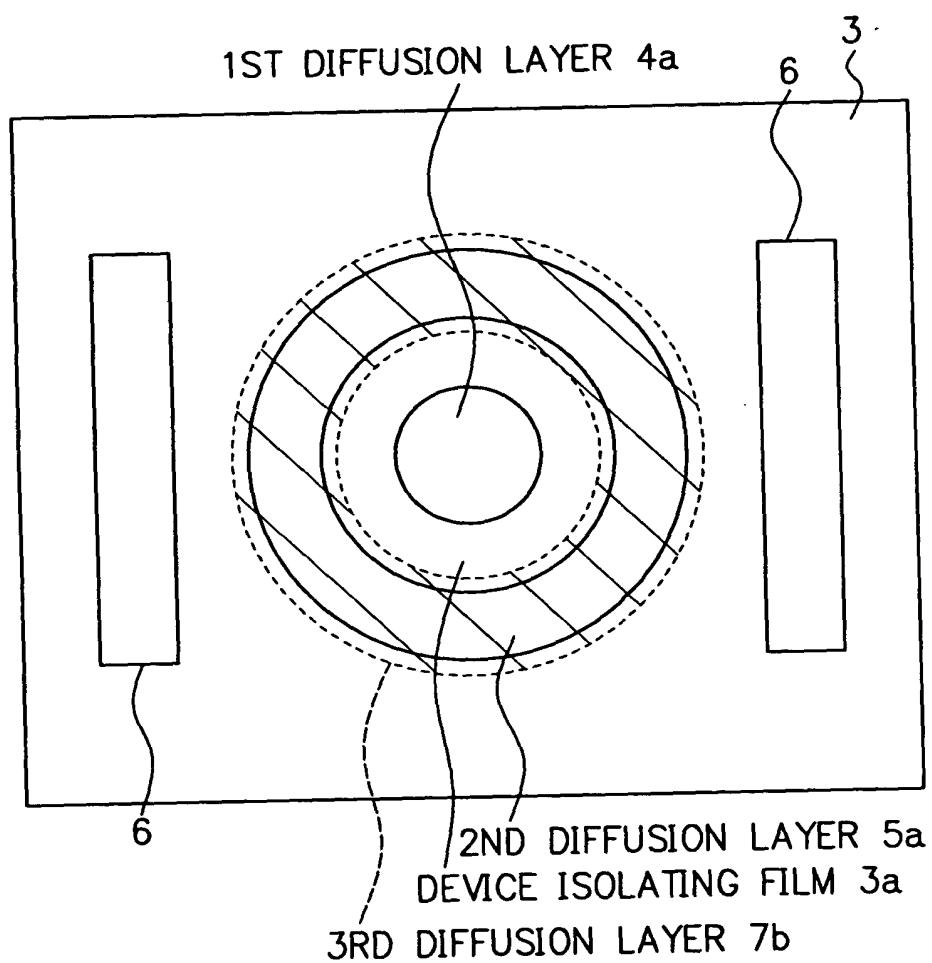


FIG. 5A

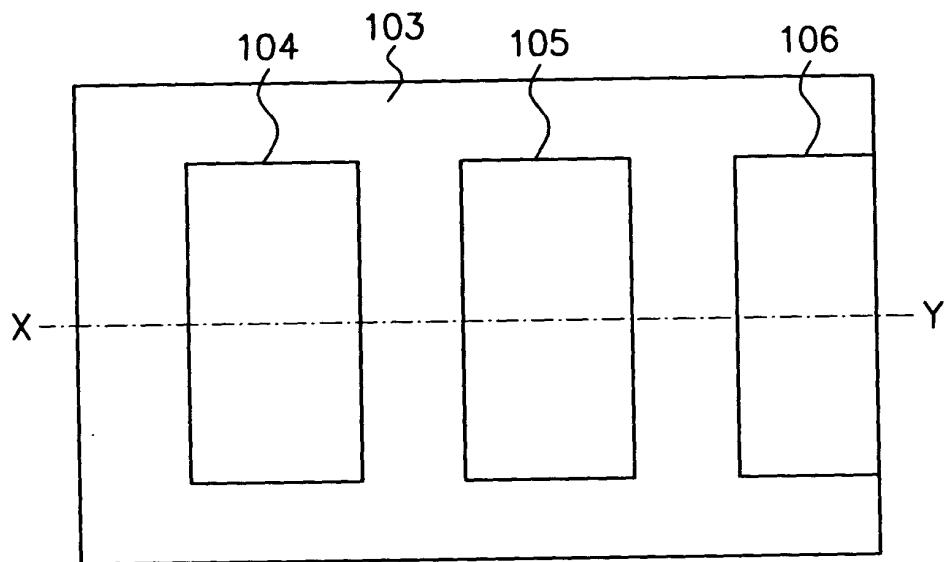


FIG. 5B

